832SLAVE PAGE 1

1 ;\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

2 ;

3 ; Author : ADI - Apps www.analog.com/MicroConverter

4 ;

5 ; Date : April 2002

6 ;

7 ; File : SPIslave.asm

8 ;

9 ; Hardware : ADuC832

10 ;

11 ; Include File : UARTIO.asm - serial I/O routines

12 ;

13 ; Description : Demonstrates an example slave mode SPI interface.

14 ; Code is intended for use with companion code file

15 ; '832mstr.asm' running on a second MicroConverter

16 ; chip. Chips must have SCLK, MOSI, MISO, & GND pins

17 ; connected together, and P3.5 pin on master must

18 ; connect to SS pin on slave.

19 ;

20 ; If using the ADuC832 eval board, you can

21 ; simply connect the 10-pin SPI header directly

22 ; to that of the master board. However, you must

23 ; also ensure that LK5 ('SS master') is REMOVED on

24 ; the slave board, and INSERTED on the master board.

25 ;

26 ; Once hardware is connected, download code to both

27 ; master & slave devices ('832mstr' to the master,

28 ; '832slave' to the slave). Reset the slave first,

29 ; and then the master. The slave will sit with the

30 ; LED off until the master starts exchanging data

31 ; with it at which time its LED will start blinking

32 ; in sync (or 180°out of phase) with that of the

33 ; master. When first launched, both master and slave

34 ; are transmitting zeros repeatedly on the SPI port.

35 ; Pressing the INT0 button on either master or slave

36 ; increments the value it is transmitting. Received

37 ; SPI data is relayed out the UART and can be viewed

38 ; on any VT100 terminal or terminal emulator at

39 ; 9600baud/8bits/noparity/1stopbit. Characters sent

40 ; from the terminal to the MicroConverter will update

41 ; the value being transmitted by SPI.

42 ;

43 ;\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

44

45 $MOD832 ; Use 8052 & ADuC832 predefined symbols

46

00B4 47 LED EQU P3.4 ; P3.4 drives red LED on eval board

48

49 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

50 ; DEFINE VARIABLES IN INTERNAL RAM

---- 51 DSEG

0060 52 ORG 0060h

0060 53 INPUT: DS 1 ; data byte received by SPI

0061 54 OUTPUT: DS 1 ; data byte to send by SPI

55

56 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

57 ; BEGINNING OF CODE

---- 58 CSEG

832SLAVE PAGE 2

59

0000 60 ORG 0000h

0000 02004B 61 JMP MAIN ; jump to main program

62

63 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

64 ; INTERRUPT VECTOR SPACE

0003 65 ORG 0003h ; (.................... INT0 ISR)

66

0003 0561 67 INC OUTPUT

0005 32 68 RETI

69

003B 70 ORG 003Bh ; (.................... SPI ISR)

71

003B 85F760 72 MOV INPUT,SPIDAT ; get data just received by SPI

003E 8561F7 73 MOV SPIDAT,OUTPUT ; update next byte to transmit

0041 C3 74 CLR C ; clear C indicates transfer complete

0042 32 75 RETI

76

77 ;====================================================================

78 ; MAIN PROGRAM

004B 79 ORG 004Bh

80

004B 81 MAIN:

82

004B 758107 83 MOV SP,#007h

84

85 ; CONFIGURE UART...

004E 759E82 86 MOV T3CON,#082h

0051 759D2D 87 Mov T3FD,#02Dh

0054 759852 88 MOV SCON,#052h

89

90 ; CONFIGURE SPI...

91

0057 75F824 92 MOV SPICON,#024h ; configure SPI port for:

93 ; CPHA=1, CPOL=0, slave

005A 75A901 94 MOV IEIP2,#1 ; enable SPI interrupt

95

96 ; CONFIGURE INTERRUPT 0...

97

005D D288 98 SETB IT0 ; INT0 edge triggered

005F D2A8 99 SETB EX0 ; enable INT0 interrupt

100

101 ; ENABLE INTERRUPTS & ENTER MAIN LOOP...

102

0061 756100 103 MOV OUTPUT,#0 ; set initial value for output byte..

0064 75F700 104 MOV SPIDAT,#0 ; ..including very fisrt output byte

0067 D2AF 105 SETB EA ; enable inturrupts

106

0069 B2B4 107 LOOP: CPL LED ; flash the LED on the eval board

006B D3 108 SETB C

006C 40FE 109 JC $ ; wait here to receive SPI transfer

006E E560 110 MOV A,INPUT ; send value received by SPI..

0070 1200A3 111 CALL SENDVAL ; ..out the UART as 2 hex chars

0073 90011E 112 MOV DPTR,#SEPERATOR ; send line-feed & crdg-return..

0076 120083 113 CALL SENDSTRING ; ..out the UART

0079 3098ED 114 JNB RI,LOOP ; repeat (unless UART data received)

115

116 ; WHEN UART DATA RECEIVED, MOVE DATA TO SPI OUTPUT...

832SLAVE PAGE 3

117

007C 859961 118 MOV OUTPUT,SBUF ; update OUTPUT byte to new value

007F C298 119 CLR RI ; must clear RI

0081 80E6 120 JMP LOOP ; back to main loop

121

122 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

123 ; SUBROUTINE INCLUDE FILE

124

=1 125 $INCLUDE(UARTIO.asm)

=1 126 ;\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

=1 127 ;

=1 128 ; Author : ADI - Apps www.analog.com/MicroConverter

=1 129 ;

=1 130 ; Date : January 2001

=1 131 ;

=1 132 ; File : UARTIO.asm

=1 133 ;

=1 134 ; Hardware : any 8051 based microcontroller or MicroConverter

=1 135 ;

=1 136 ; Description : standard UART I/O subroutines. total size of this

=1 137 ; code when assembled is 155 bytes. routines for use

=1 138 ; external to this file are:

=1 139 ;

=1 140 ; SENDSTRING - sends a string of characters

=1 141 ; SENDCHAR - sends a single character

=1 142 ; SENDVAL - sends a byte as 2 ASCII characters

=1 143 ; HEX2ASCII - converts from HEX to ASCII

=1 144 ; ASCII2HEX - converts from ASCII to HEX

=1 145 ; GETCHAR - gets a single character

=1 146 ; GETVAL - gets a byte as 2 ASCII characters

=1 147 ;

=1 148 ;\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

=1 149

=1 150 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 151 ; SENDSTRING

=1 152

0083 =1 153 SENDSTRING: ; sends ASCII string to UART starting at location

=1 154 ; DPTR and ending with a null (0) value

=1 155

0083 C0E0 =1 156 PUSH ACC

0085 C0F0 =1 157 PUSH B

0087 E4 =1 158 CLR A

0088 F5F0 =1 159 MOV B,A

008A E5F0 =1 160 IO0010: MOV A,B

008C 05F0 =1 161 INC B

008E 93 =1 162 MOVC A,@A+DPTR

008F 6005 =1 163 JZ IO0020

0091 12009B =1 164 CALL SENDCHAR

0094 80F4 =1 165 JMP IO0010

0096 D0F0 =1 166 IO0020: POP B

0098 D0E0 =1 167 POP ACC

=1 168

009A 22 =1 169 RET

=1 170

=1 171 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 172 ; SENDCHAR

=1 173

009B =1 174 SENDCHAR: ; sends ASCII value contained in A to UART

832SLAVE PAGE 4

=1 175

009B 3099FD =1 176 JNB TI,$ ; wait til present char gone

009E C299 =1 177 CLR TI ; must clear TI

00A0 F599 =1 178 MOV SBUF,A

=1 179

00A2 22 =1 180 RET

=1 181

=1 182 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 183 ; SENDVAL

=1 184

00A3 =1 185 SENDVAL: ; converts the hex value of A into two ASCII chars,

=1 186 ; and then spits these two characters up the UART.

=1 187 ; does not change the value of A.

=1 188

00A3 C0E0 =1 189 PUSH ACC

00A5 C4 =1 190 SWAP A

00A6 1200B7 =1 191 CALL HEX2ASCII

00A9 119B =1 192 CALL SENDCHAR ; send high nibble

00AB D0E0 =1 193 POP ACC

00AD C0E0 =1 194 PUSH ACC

00AF 1200B7 =1 195 CALL HEX2ASCII

00B2 119B =1 196 CALL SENDCHAR ; send low nibble

00B4 D0E0 =1 197 POP ACC

=1 198

00B6 22 =1 199 RET

=1 200

=1 201 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 202 ; HEX2ASCII

=1 203

00B7 =1 204 HEX2ASCII: ; converts A into the hex character representing the

=1 205 ; value of A's least significant nibble

=1 206

00B7 540F =1 207 ANL A,#00Fh

00B9 B40A00 =1 208 CJNE A,#00Ah,$+3

00BC 4002 =1 209 JC IO0030

00BE 2407 =1 210 ADD A,#007h

00C0 2430 =1 211 IO0030: ADD A,#'0'

=1 212

00C2 22 =1 213 RET

=1 214

=1 215 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 216 ; ASCII2HEX

=1 217

00C3 =1 218 ASCII2HEX: ; converts A from an ASCII digit ('0'-'9' or 'A'-'F')

=1 219 ; into the corresponding number (0-15). returns C=1

=1 220 ; when input is other than an ASCII digit,

=1 221 ; indicating invalid output (returned as 255).

=1 222

00C3 C3 =1 223 CLR C

00C4 9430 =1 224 SUBB A,#'0'

00C6 B40A00 =1 225 CJNE A,#10,$+3

00C9 401B =1 226 JC IO0050 ; if '0'<=char<='9', return OK

00CB B41100 =1 227 CJNE A,#17,$+3

00CE 4013 =1 228 JC IO0040 ; if '9'<char<'A', return FAIL

00D0 9407 =1 229 SUBB A,#7

00D2 B41000 =1 230 CJNE A,#10h,$+3

00D5 400F =1 231 JC IO0050 ; if 'A'<=char<='F', return OK

00D7 B42A00 =1 232 CJNE A,#42,$+3

832SLAVE PAGE 5

00DA 4007 =1 233 JC IO0040 ; if 'F'<char<'a', return FAIL

00DC 9420 =1 234 SUBB A,#20h

00DE B41000 =1 235 CJNE A,#10h,$+3

00E1 4003 =1 236 JC IO0050 ; if 'a'<=char<='f', return OK..

=1 237

00E3 C3 =1 238 IO0040: CLR C ; ..else return FAIL

00E4 74FF =1 239 MOV A,#0FFh

=1 240

00E6 B3 =1 241 IO0050: CPL C

00E7 22 =1 242 RET

=1 243

=1 244 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 245 ; GETCHAR

=1 246

00E8 =1 247 GETCHAR: ; waits for a single ASCII character to be received

=1 248 ; by the UART. places this character into A.

=1 249

00E8 3098FD =1 250 JNB RI,$

00EB E599 =1 251 MOV A,SBUF

00ED C298 =1 252 CLR RI

=1 253

00EF 22 =1 254 RET

=1 255

=1 256 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

=1 257 ; GETVAL

=1 258

00F0 =1 259 GETVAL: ; waits for two ASCII hex digits to be received by

=1 260 ; the UART. returns the hex value in A.

=1 261

00F0 C0F0 =1 262 PUSH B

00F2 C000 =1 263 PUSH 0

00F4 C298 =1 264 IO0060: CLR RI

00F6 11E8 =1 265 CALL GETCHAR ; first nibble

00F8 F500 =1 266 MOV 0,A ; store received char

00FA 11C3 =1 267 CALL ASCII2HEX

00FC 40F6 =1 268 JC IO0060 ; if not '0' thru 'F', don't accept

00FE C4 =1 269 SWAP A ; swap nibbles

00FF F5F0 =1 270 MOV B,A ; store nibble in B

0101 E500 =1 271 MOV A,0 ; echo received char

0103 119B =1 272 CALL SENDCHAR

0105 C298 =1 273 IO0070: CLR RI

0107 11E8 =1 274 CALL GETCHAR ; second nibble

0109 F500 =1 275 MOV 0,A ; store received char

010B 11C3 =1 276 CALL ASCII2HEX

010D 40F6 =1 277 JC IO0070 ; if not '0' thru 'F', don't accept

010F 45F0 =1 278 ORL A,B ; combine nibbles

0111 F5F0 =1 279 MOV B,A ; store results in B

0113 E500 =1 280 MOV A,0 ; echo received char

0115 119B =1 281 CALL SENDCHAR

0117 E5F0 =1 282 MOV A,B ; final result

0119 D000 =1 283 POP 0

011B D0F0 =1 284 POP B

=1 285

011D 22 =1 286 RET

=1 287

288

289 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

290 ; TEXT DATA TABLES

832SLAVE PAGE 6

291

011E 0A0D00 292 SEPERATOR: DB 10,13,0

293

294 ;\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

295

296 END

297

VERSION 1.2h ASSEMBLY COMPLETE, 0 ERRORS FOUND

832SLAVE PAGE 7

ACC. . . . . . . . . . . . . . . D ADDR 00E0H PREDEFINED

ASCII2HEX. . . . . . . . . . . . C ADDR 00C3H

B. . . . . . . . . . . . . . . . D ADDR 00F0H PREDEFINED

EA . . . . . . . . . . . . . . . B ADDR 00AFH PREDEFINED

EX0. . . . . . . . . . . . . . . B ADDR 00A8H PREDEFINED

GETCHAR. . . . . . . . . . . . . C ADDR 00E8H

GETVAL . . . . . . . . . . . . . C ADDR 00F0H NOT USED

HEX2ASCII. . . . . . . . . . . . C ADDR 00B7H

IEIP2. . . . . . . . . . . . . . D ADDR 00A9H PREDEFINED

INPUT. . . . . . . . . . . . . . D ADDR 0060H

IO0010 . . . . . . . . . . . . . C ADDR 008AH

IO0020 . . . . . . . . . . . . . C ADDR 0096H

IO0030 . . . . . . . . . . . . . C ADDR 00C0H

IO0040 . . . . . . . . . . . . . C ADDR 00E3H

IO0050 . . . . . . . . . . . . . C ADDR 00E6H

IO0060 . . . . . . . . . . . . . C ADDR 00F4H

IO0070 . . . . . . . . . . . . . C ADDR 0105H

IT0. . . . . . . . . . . . . . . B ADDR 0088H PREDEFINED

LED. . . . . . . . . . . . . . . NUMB 00B4H

LOOP . . . . . . . . . . . . . . C ADDR 0069H

MAIN . . . . . . . . . . . . . . C ADDR 004BH

OUTPUT . . . . . . . . . . . . . D ADDR 0061H

P3 . . . . . . . . . . . . . . . D ADDR 00B0H PREDEFINED

RI . . . . . . . . . . . . . . . B ADDR 0098H PREDEFINED

SBUF . . . . . . . . . . . . . . D ADDR 0099H PREDEFINED

SCON . . . . . . . . . . . . . . D ADDR 0098H PREDEFINED

SENDCHAR . . . . . . . . . . . . C ADDR 009BH

SENDSTRING . . . . . . . . . . . C ADDR 0083H

SENDVAL. . . . . . . . . . . . . C ADDR 00A3H

SEPERATOR. . . . . . . . . . . . C ADDR 011EH

SP . . . . . . . . . . . . . . . D ADDR 0081H PREDEFINED

SPICON . . . . . . . . . . . . . D ADDR 00F8H PREDEFINED

SPIDAT . . . . . . . . . . . . . D ADDR 00F7H PREDEFINED

T3CON. . . . . . . . . . . . . . D ADDR 009EH PREDEFINED

T3FD . . . . . . . . . . . . . . D ADDR 009DH PREDEFINED

TI . . . . . . . . . . . . . . . B ADDR 0099H PREDEFINED